

Original Article

# High Gain Active Switched Network-Based Boost Converter for Dc Microgrids: Hardware Design and Setup

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**Abstract:** In this project, we present a novel high-gain active switched network-based boost converter designed for DC microgrid applications. This converter addresses the limitations of traditional boost converters, which often struggle with achieving high voltage gains while maintaining efficiency and reliability. Our proposed converter integrates a switched network configuration, which enhances the voltage gain significantly without the need for extreme duty cycles or multiple cascaded stages. The active switched network configuration allows for the redistribution of voltage stresses across multiple components, thereby improving the overall efficiency and reducing the size and cost of passive components. Additionally, this design incorporates advanced control strategies to ensure stable operation under varying load and input conditions, which is crucial for the dynamic environment of DC microgrids.

**Keywords:** High-Gain Boost Converter, Active Switched Network, Dc Microgrids, Boost Converter Design, Power Electronics Hardware, Microgrid Optimization, Switching Network Converter, Hardware Setup For Boost Converters.

## INTRODUCTION

The electrical grid is going through a revolution since the years 2000s. Such revolution is composed first by economic aspects as the liberalization of power markets and the unbundling and privatization of previously state-owned power companies. Next, because of environmental and social concerns, there has been a choice for reducing the use of fossil-based sources, and to some extent even nuclear power. Such merge of traditional power grids with ICT is now known as Smart Grids. Smart Grids are composed of several elements, among them the concept of Micro Grids. Micro Grids are small portions of the electric grid that can to some extent balance itself with the production and consumption of electricity and can stabilize its fundamental states. In the case where the Micro Grid is always connected to the main grid, thus importing and exporting arbitrary amounts of power, the Micro Grid is said to be in grid-connected mode. But when larger shares of renewables are present in such grids, the problem may become very complex. Renewables have some characteristics that completely distinguish them from other power sources. The most important is the fact that renewables are not controlled. Wind, sun, tides, and other natural phenomena are uncorrelated with the needs of consumers.

Thus, renewables are mostly integrated into the low and medium voltage levels, which is in complete antagonism with the way power systems were designed. Another important aspect is the significant number of people in remote communities living without access to electricity. These villages may never have grid connection because of economic reasons and remoteness. On the other hand, many of isolated communities, such as African, Brazilian, or island communities, have great potential for solar radiation and wind, allowing for the use of renewables. Electricity supply can be seen as a contribution to social inclusion and improvement of the quality of life where electricity is mainly used for household purposes such as lighting, heating, and others to meet local energy demand. In addition, the transition for an electricity based energy usage avoids consuming local carbon-based resources like coal or wood. The result would be less expensive materials, and better efficiency (fewer losses). Also, direct current can be more efficient due to its simpler topology; the absence of reactive power and frequency to be controlled; the harmonic distortion is not a problem anymore; and there is no need of synchronization with the network. The consequence is a simpler control structure based on the interaction of currents between the converters, being the DC bus voltage the main control priority, that is, the voltage is a natural indicator of power balance conditions. At the same time, the DC Micro Grid is a challenge because the structure of the current power grid, power supplies, transformers, cables, and protection is designed in alternating current.



For this reason, hybrid AC/DC Micro Grid is seen as a compromise between AC and DC to allow for better integration of these new devices and the classical electric grid.

## SYSTEM IMPLEMENTATION

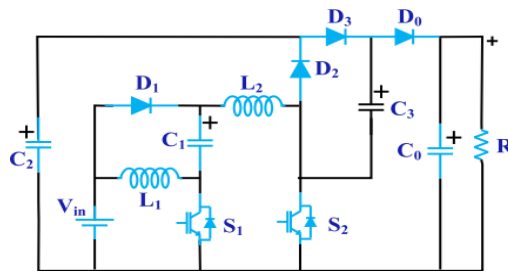
### 2.1 Existing System:

- The existing boost converters typically utilize traditional topologies like the basic non-isolated boost converter or the interleaved boost converter.
- The conventional Cascade boost, quadratic boost, traditional boost converter combined with switched-capacitor technique, voltage lift, and capacitor-diode voltage multiplier are a few most commonly used non-isolated high gain converters.

### 2.2 Proposed System:

- This project proposes a novel converter topology with reduced current stress across active switches to provide a stable constant dc voltage. The proposed topology has the advantage of providing a high voltage gain, low current stress, and low conduction loss on the active switches, simplified control, and high efficiency.
- The current is equally shared by both the switches and thereby reducing the conduction loss. The proposed converter topology is a transformer-less design.
- Both the switches are connected in parallel and thereby reducing the switch current stress. Therefore, the power circuit of the proposed converter can be designed by using low current rating switches.

### 2.3 Proposed Converter:



### 2.4 Proposed Topology:

- It is consisting of two inductors  $L_1$  and  $L_2$  which have the same inductance value and switch  $S_1$  and switch  $S_2$  are both being turned ON and OFF at once.
- There are four diodes ( $D_0$  to  $D_3$ ) and four capacitors ( $C_0$  to  $C_3$ ) in the circuit. The working principles and the steady-state analysis of the proposed converter in both CCM and DCM .
- First of all, considering all the circuit components to be ideal. Neglecting the ON-state resistance of the active switches, the forward voltage drop of the diodes and the effective series resistance (ESR) of the inductors and capacitors.
- However, it is assumed that both the inductors have equal inductance value and all the capacitors are large enough, and the capacitor voltages are considered to be constant.

### 2.5 Mode of Operation:

- The proposed converter consisting of two switches that are operating at the same time with the same duty pulse and duty ratio. Therefore, the proposed converter has two operating modes in CCM as

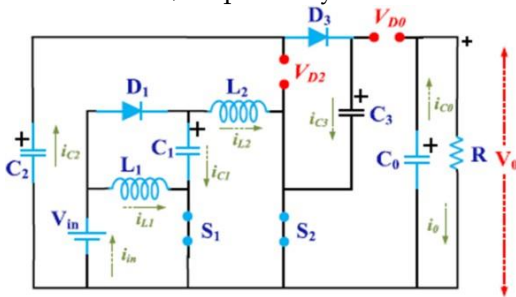
CCM 1  
CCM 2

#### 2.5.1 Ccm1:

- The switch  $S_1$  and switch  $S_2$  both are kept ON during mode I. The equivalent circuit of the proposed converter for this mode is displayed in Fig.
- The input supply  $V_{in}$  charges inductor  $L_1$  via switch  $S_1$ , the capacitor  $C_1$  via diode  $D_1$  and

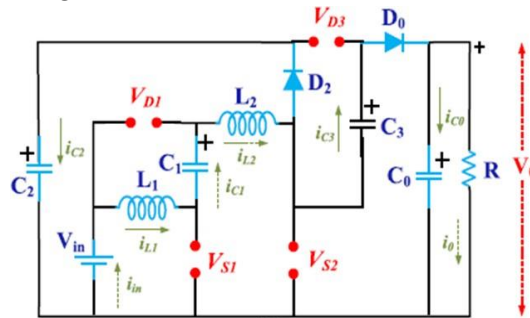
switch S1, and inductor L2 via diode D1 and switch S2, respectively.

- Simultaneously, capacitor C3 is charged by capacitor C2 via diode D3 and switch S2, and the energy stored in capacitor C0 is transferred to the load R.
- Therefore, the voltages across the inductors L1 and L2, and capacitors C1 and C3 can be expressed as,  $V_{L1} = V_{L2} = V_{in}$ ,  $V_{C1} = V_{in}$ ,  $V_{C3} = V_{C2}$  where,  $V_{L1}$  and  $V_{L2}$  are the voltages across inductors L1 and L2, respectively; the voltages across capacitors C1 and C3 are  $V_{C1}$  and  $V_{C3}$ , respectively

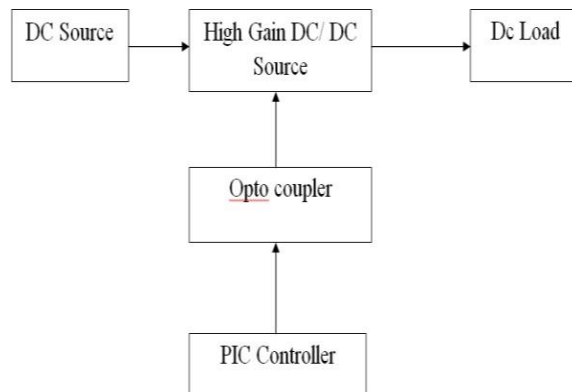


### 2.5.2 Ccm2:

- During mode 2, switch S1 and switch S2 are both being turned OFF simultaneously.
- Mode 2 equivalent circuit of the proposed converter is displayed in Fig.
- In mode 2, the input supply  $V_{in}$  charges the output capacitor C0, inductor L1, capacitor C1, inductor L2, and capacitor C3 via diode D0.
- At the same time, capacitor C2 is charged by the input supply voltage  $V_{in}$ , inductor L1, capacitor C1, and inductor L2 through diode D2



## 2.6 Block Diagram Of Hardware:



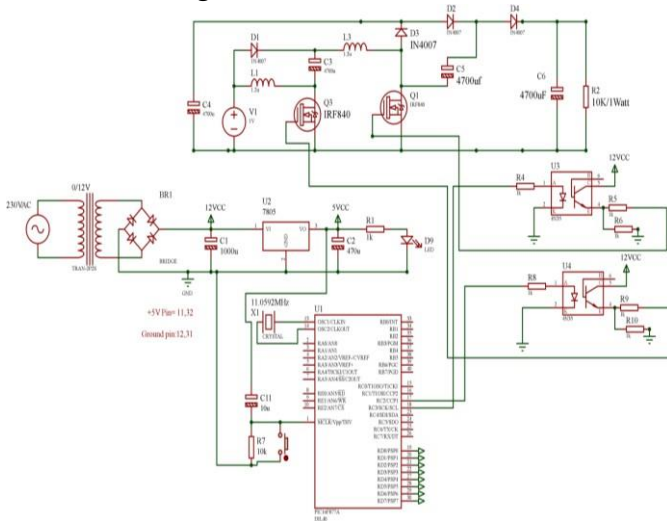
### 2.6.1 Block Diagram Description:

- DC Source: This is the primary power source for the microgrid, providing a DC voltage. It could be a solar panel, a battery bank, or any other DC power source.
- High Gain DC/DC Converter: This converter steps up or steps down the DC voltage to match the requirements of the load. High gain refers to its capability to handle significant voltage differences

between input and output.

- **Load:** This is the device or set of devices that consume power from the microgrid. It could be DC appliances, batteries, or other electronics requiring specific voltages.
- **Optocoupler (Optoisolator):** This component provides electrical isolation between different parts of the circuit. It transmits signals using light waves to prevent high voltages from affecting the control side of the circuit.
- **PIC Controller:** A microcontroller from the PIC (Peripheral Interface Controller) series, which manages the operation of the microgrid. It monitors the voltage levels, controls the DC/DC converter, and ensures efficient power distribution.

## 2.7 Circuit Diagram:



### 2.7.1 Circuit Diagram Description:

**DC Source Connection** The positive terminal of the DC source is connected to the input of the high gain DC/DC converter. The negative terminal of the DC source is connected to the ground (common reference point) of the circuit. **DC/DC Converter** The DC source's positive terminal connects to the input of the DC/DC converter. The output of the DC/DC converter is connected to the load, supplying it with the required voltage. The ground of the DC/DC converter is connected to the circuit ground. **Load** The load is connected across the output of the DC/DC converter. One terminal of the load is connected to the positive output of the converter, and the other terminal is connected to the ground. **Opto coupler** The opto coupler has an LED on the input side and a phototransistor on the output side. The input (LED side) of the opto coupler is connected to the output of the DC source or converter through a current-limiting resistor. **PIC Controller** The PIC controller is powered by a regulated low voltage supply (typically 5V or 3.3V). It receives signals from the opto coupler to monitor the voltage or current levels. The controller sends PWM (Pulse Width Modulation) signals to the DC/DC converter to adjust its operation based on the feedback from the opto coupler. It can also be programmed to perform other control tasks, such as load shedding, fault detection, and communication with other micro grid components.

## HARDWARE DETAILS

### 3.1 DC To DC Boost Converter:

The low input DC voltage is converted into high output DC voltage using DC to DC boost converter. As the input voltage is stepped up compared to output voltage, hence, it is also called as a step up converter. Generally, DC to DC converters can be designed using power semiconductor switching devices and discrete electrical and electronics components.

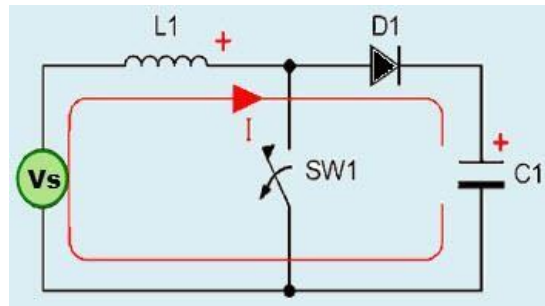


Figure. 3.1 DC to DC Boost Converter Continuous Conduction Mode Circuit

The continuous conduction mode circuit of the DC to DC boost converter is shown in the figure that consists of an inductor, capacitor, switching device, diode, and input voltage source. This boost converter circuit switch is controlled using a pulse width modulator (PWM). If this switch is in ON state, then energy will be developed in the inductor and thus more energy will be delivered to the output.

### 3.2 Micro Controller Pic16f877a:

PIC 16F877 is one of the most advanced microcontroller from Microchip. This controller is widely used for experimental and modern applications because of its low price, wide range of applications, high quality, and ease of availability. It is ideal for applications such as machine control applications, measurement devices, study purpose, and so on.

#### 3.2.1 PIC Controller Pin Diagram:

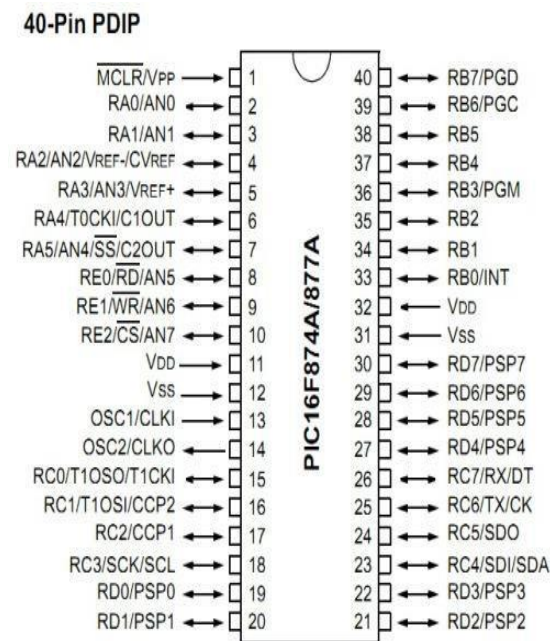


Figure. 3.2 PIC Controller Pin Diagram

### 3.3 Mct2e - Phototransistor Optocoupler :

MCT2E is a phototransistor Optocoupler, as the name "phototransistor" suggests it has a transistor which is controlled based on light (photon). When the IR led is powered the light from it falls on the transistor and it conducts. The arrangement and pin-outs of the IR LED and the photo-transistor is shown below.

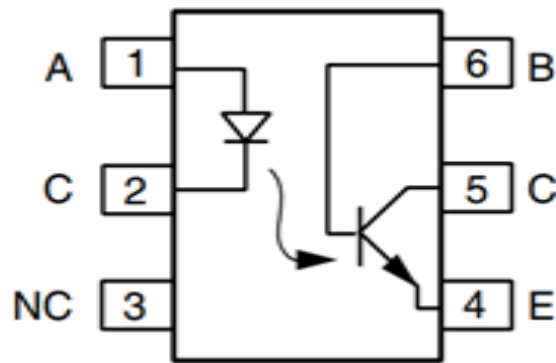


Figure. 3.3 Optocoupler

This IC is used to provide electrical isolation between two circuits, one part of the circuit is connected to the IR LED and the other to Photo-transistor. The digital signal given to the IR LED will be reflected on the transistor but there will be no hard electrical connection between the two. This comes in very handy when you are trying to isolate a noisy signal from your digital electronics, so if you are looking for an IC to provide optical isolation in your circuit design then this IC might be the right choice for you.

### 3.4 Single Power Supply:

Power supply gives supply to all components. It is used to convert AC voltage into DC voltage. Transformer used to convert 230V into 12V AC. 12V AC is given to diode. Diode range is 1N4007, which is used to convert AC voltage into DC voltage. AC capacitor used to charge AC components and discharge on ground. LM 7805 regulator is used to maintain voltage as constant. Then signal will be given to next capacitor, which is used to filter unwanted AC component. Load will be LED and resistor. LED voltage is 1.75V. If voltage is above level beyond the limit, and then it will be dropped on resistor.

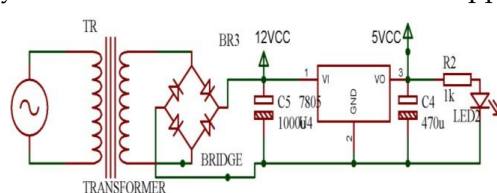


Figure. 3.4 Single Power Supply Circuit

### 3.5 Mosfet(Irf840):

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness. The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50

W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

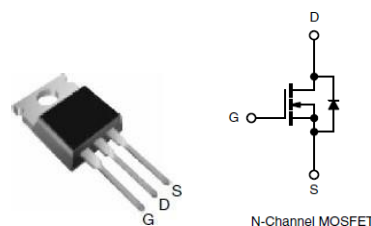


Figure. 3.5 Mosfet(Irf840)

## RESULTS

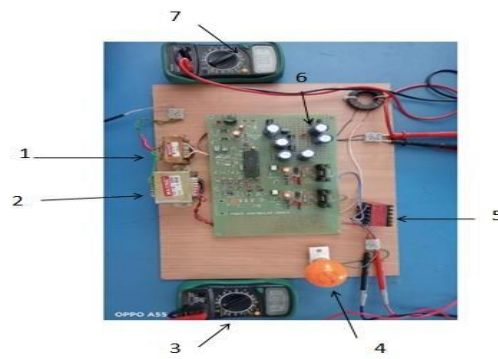


Figure 4.1: Hardware Prototype Model

The above figure 4.1 "A New High Gain Active Switched Network-Based Boost Converter for DC Micro grid" hardware model used components

1. Power supply Transformer(0-9V) step down transformer
2. Converter Input Power transformer 12-0-12V 3Amps
3. Output Meter
4. Resistive Load
5. Inductor 18mH
6. Rectifier



Figure 4.2: Input DC Voltage

The figure 4.2 is Input DC voltage (12V) for DC/DC converter for our prototype model.



Figure 4.3: Gate Pulse

The above figure 4.3 gate pulse for our hardware model MOSFET. The PWM pulse 10Khz infrequency and the amplitude 5V.



Figure 4.4: Output DC Voltage

The figure 4.4 is Output DC voltage (164V) for DC/DC converter for our prototype model. Our project High gain DC/DC converter is minimum 4 times increase of the output voltage. Our prototype model input 12V and output voltage 164V.

### CONCLUSION

The DC-DC high gain boost converter with active switched inductor network and Model Predictive control for DC microgrid has been presented in the paper. The proposed converter has utilized two switches to reduce the current stress. The active switched inductor network and voltage multiplier structure have been effectively arranged to boost the output voltage and for the equal distribution of capacitor voltage stress. Furthermore, the presented topology drains a continuous current from the input supply. Hence, high-voltage boost ability and continuous input current make it suitable for DC source applications. A detailed CCM analysis of the proposed converter has been presented. The hardware prototype model has been made and tested successfully.

### REFERENCES

- [1] Triptendu Chaudhury and Debaprasad Kastha , "A High Gain Multiport DC-DC Converter for Integrating EnergyStorage Devices to DC Microgrid",2020.
- [2] Shima Sadaf ,Nasser Al-Emadi , Pandav Kiran Maroti , Atif Iqbal , "A New High Gain Active Switched Network-Based Boost Converter For Dc Microgrid Application",2021.
- [3] Farhan Mumtaz, "A Novel Non-Isolated High-Gain Non-Inverting Interleaved DC-DC Converter",2023.
- [4] Javed Ahmad "A New High-Gain DC-DC Converter with Continuous Input Current for DC Microgrid Applications",2021.
- [5] G.Amrita,G. Kanimozhi"Modified High Gain DC-DC Converter with APICs for Microgrid",2022.
- [6] J. Gnanavadeivel "Analysis and design of high gain DC-DC converter for renewable energy applications",2023.
- [7] B. N. Ch. V. Chakravarthi and G. V. Siva Krishna Rao,"A High Gain Novel Double-Boost Converter for DC MicrogridApplications"2020.
- [8] S. Uday Kiran, P. Manoj Kumar, Y. V. Pavan Kumar,"Comprehensive Analysis on Critical Factors for the Operation of Advanced High Gain DC-DC Converters Used in Renewable Energy Applications",2021.
- [9] Chanthathi, Sasibhushan Rao. (2021). How the Power of Machine - Machine Learning, Data Science and NLP Can Be Used to Prevent Spoofing and Reduce Financial Risks. 10.13140/RG.2.2.18761.76640.
- [10] Dhafer Almkhles "Dynamic Analysis of Extendable Hybrid Voltage Lift DC-DC Converter for DC Microgrid",2022.
- [11] Mansour AS,"High gain DC/DC converter with continuous input current for renewable energy applications",2022.
- [12] Naga Ramesh Palakurti, 2023. "Evolving Drug Discovery: Artificial Intelligence and Machine Learning's Impact in Pharmaceutical Research" *ESP Journal of Engineering & Technology Advancements* 3(3): 136-147. [Link]
- [13] Naga Ramesh Palakurti, 2022. "AI Applications in Food Safety and Quality Control" *ESP Journal of Engineering & Technology Advancements* 2(3): 48-61. [Link]
- [14] Chanthathi, S. R. (2024). An automated process in building organic branding opportunity, budget Intensity, recommendation in seasons with Google trends data. Sasibhushan Rao Chanthathi. <https://doi.org/10.30574/wjaets.2024.12.2.0326>
- [15] Kumar Shukla, Nimeshkumar Patel, Hirenkumar Mistry, 2024." *Securing The Cloud: Strategies and Innovations In Network Security For Modern Computing Environments*" Volume 11, Issue 04 pp. 1786-1796. [Link]

- [16] Muthukumaran Vaithianathan, Mahesh Patil, Shunye Frank Ng, Shiv Udkar, 2024. "Verification of Low-Power Semiconductor Designs Using UVM", *ESP Journal of Engineering & Technology Advancements* 4(3): 28-44.
- [17] Doctor, A., B. Vondenbusch, and J. Kozak. "Bone segmentation applying rigid bone position and triple shadow check method based on RF data." *Acta of Bioengineering and Biomechanics*, 13.2 (2011): 3-11.
- [18] Jaseem Pookandy, Enhancing Customer Relationship Management with Salesforce: A Comprehensive Review, *International Journal of Computer Engineering and Technology (IJCET)*, 15(4), 2024, pp. 64-84
- [19] Muthukumaran Vaithianathan, Mahesh Patil, Shunye Frank Ng, Shiv Udkar, 2024. "Energy-Efficient FPGA Design for Wearable and Implantable Devices" *ESP International Journal of Advancements in Science & Technology (ESP-IJAST)* Volume 2, Issue 2: 37-51.
- [20] Jacopo Pianigiani, Michal Styszynski, Atul S Moghe, Joseph Williams, Sahana Sekhar Palagrahara Chandrashekar, Tong Jiang, Rishabh Ramakant Tulsian, Manish Krishnan, Soumil Ramesh Kulkarni, Vinod Nair, Jeba Paulaiyan, Sukhdev S. Kapur, Ashok Ganesan, 2020. *Automation of Maintenance Mode Operations for Network Devices*, US10742501B1. [Link]
- [21] Chandrakanth Lekkala, "Utilizing Cloud - Based Data Warehouses for Advanced Analytics: A Comparative Study", *International Journal of Science and Research (IJSR)*, Volume 11 Issue 1, January 2022, pp. 1639-1643, <https://www.ijsr.net/getabstract.php?paperid=SR24628182046>
- [22] Julian, Anitha, Mary, Gerardine Immaculate, Selvi, S., Rele, Mayur & Vaithianathan, Muthukumaran (2024) Blockchain based solutions for privacy-preserving authentication and authorization in networks, *Journal of Discrete Mathematical Sciences and Cryptography*, 27:2-B, 797-808, DOI: 10.47974/JDMSC-1956
- [23] Muthukumaran Vaithianathan, 2024. "Digital Signal Processing for Noise Suppression in Voice Signals", *IJCSPUB - INTERNATIONAL JOURNAL OF CURRENT SCIENCE* (www.IJCSPUB.org), ISSN: 2250-1770, Vol.14, Issue 2, page no.72-80, April-2024, Available: <https://rjpn.org/IJCSPUB/papers/IJCSP24B1010.pdf>
- [24] Muthukumaran Vaithianathan, "Real-Time Object Detection and Recognition in FPGA-Based Autonomous Driving Systems," *International Journal of Computer Trends and Technology*, vol. 72, no. 4, pp. 145-152, 2024. Crossref, <https://doi.org/10.14445/22312803/IJCTT-V72I4P119>
- [25] Muthukumaran Vaithianathan, Mahesh Patil, Shunye Frank Ng, Shiv Udkar, 2023. "Comparative Study of FPGA and GPU for High-Performance Computing and AI" *ESP International Journal of Advancements in Computational Technology (ESP-IJACT)* Volume 1, Issue 1: 37-46. [PDF]
- [26] Muthukumaran Vaithianathan, Mahesh Patil, Shunye Frank Ng, Shiv Udkar, 2024. "Low-Power FPGA Design Techniques for Next-Generation Mobile Devices" *ESP International Journal of Advancements in Computational Technology (ESP-IJACT)* Volume 2, Issue 2: 82-93. [PDF]
- [27] Dhamotharan Seenivasan, Muthukumaran Vaithianathan, 2023. "Real-Time Adaptation: Change Data Capture in Modern Computer Architecture" *ESP International Journal of Advancements in Computational Technology (ESP-IJACT)* Volume 1, Issue 2: 49-61. [PDF]
- [28] Muthukumaran Vaithianathan, Mahesh Patil, Shunye Frank Ng, Shiv Udkar, 2024. "Integrating AI and Machine Learning with UVM in Semiconductor Design" *ESP International Journal of Advancements in Computational Technology (ESP-IJACT)* Volume 2, Issue 3: 37-51. [PDF]
- [29] Chanthati, Sasibhushan Rao. (2021). *A segmented approach to encouragement of entrepreneurship using data science*. *World Journal of Advanced Engineering Technology and Sciences*. <https://doi.org/10.30574/wjaets.2024.12.2.0330>, [link]
- [30] Patel, N. (2024, March). SECURE ACCESS SERVICE EDGE(SASE): "EVALUATING THE IMPACT OF CONVERGED NETWORK SECURITY ARCHITECTURES IN CLOUD COMPUTING." *Journal of Emerging Technologies and Innovative Research*. <https://www.jetir.org/papers/JETIR2403481.pdf>
- [31] Vishwanath Gojanur, Aparna Bhat, "Wireless Personal Health Monitoring System", *IJETCAS: International Journal of Emerging Technologies in Computational and Applied Sciences*, eISSN: 2279-0055, pISSN: 2279-0047, 2014. [Link]
- [32] Mistry, H., Shukla, K., & Patel, N. (2024). Transforming Incident Responses, Automating Security Measures, and Revolutionizing Defence Strategies through AI-Powered Cybersecurity. *Journal of Emerging Technologies and Innovative Research*, 11(3), 25. <https://www.jetir.org/>
- [33] Aparna Bhat, "Comparison of Clustering Algorithms and Clustering Protocols in Heterogeneous Wireless Sensor Networks: A Survey," 2014 *INTERNATIONAL JOURNAL OF SCIENTIFIC PROGRESS AND RESEARCH (IJSPR)*-ISSN : 2349-4689 Volume 04- NO.1, 2014. [Link]
- [34] Shashikant Tank Kumar Mahendrabhai Shukla, Nimeshkumar Patel, Veeral Patel, 2024. "AI BASED CYBER SECURITY DATA ANALYTIC DEVICE", 414425-001, [Link]
- [35] Aparna Bhat, Rajeshwari Hegde, "Comprehensive Study of Renewable Energy Resources and Present Scenario in India," 2015 *IEEE International Conference on Engineering and Technology (ICETECH)*, Coimbatore, TN, India, 2015. [Link]
- [36] Sarangkumar Radadia Kumar Mahendrabhai Shukla, Nimeshkumar Patel, Hirenkumar Mistry, Keyur Dodiya 2024. "CYBER SECURITY DETECTING AND ALERTING DEVICE", 412409-001, [Link]
- [37] Aparna K Bhat, Rajeshwari Hegde, 2014. "Comprehensive Analysis Of Acoustic Echo Cancellation Algorithms On DSP Processor", *International Journal of Advance Computational Engineering and Networking (IJACEN)*, volume 2, Issue 9, pp.6-11. [Link]

- [38] Nimeshkumar Patel, 2022." QUANTUM CRYPTOGRAPHY IN HEALTHCARE INFORMATION SYSTEMS: ENHANCING SECURITY IN MEDICAL DATA STORAGE AND COMMUNICATION", Journal of Emerging Technologies and Innovative Research, volume 9, issue 8, pp.g193-g202. [Link]
- [39] Bhat, A., & Gojanur, V. (2015). Evolution Of 4g: A Study. International Journal of Innovative Research in ComputerScience & Engineering (IJIRCSE). Booth, K. (2020, December 4). How 5G is breaking new ground in the construction industry. BDC Magazine.<https://bdcmagazine.com/2020/12/how-5g-is-breaking-new-ground-in-the-constructionindustry/>. [Link]
- [40] Nimeshkumar Patel, 2021." SUSTAINABLE SMART CITIES: LEVERAGING IOT AND DATA ANALYTICS FOR ENERGY EFFICIENCY AND URBAN DEVELOPMENT", Journal of Emerging Technologies and Innovative Research, volume 8, Issue 3, pp.313-319. [Link]
- [41] Bhat, A., Gojanur, V., & Hegde, R. (2014). 5G evolution and need: A study. In International conference on electrical, electronics, signals, communication and optimization (EESCO) –2015.[Link]
- [42] A. Bhat, V. Gojanur, and R. Hegde. 2015. 4G protocol and architecture for BYOD over Cloud Computing. In Communications and Signal Processing (ICCSP), 2015 International Conference on. 0308-0313. Google Scholar. [Link]
- [43] M. Hindka, "Securing the Digital Backbone: An In-depth Insights into API Security Patterns and Practices", Computer Science and Engineering, Vol. 14, No. 2, pp. 35-41, 2024.
- [44] M. Hindka, "Design and Analysis of Cyber Security Capability Maturity Model", International Research Journal of Modernization in Engineering Technology and Science, Vol. 6, No. 3, pp. 1706-1710, 2024.
- [45] Hindka, M. (2024, June). Optimization Accuracy of Secured Cloud Systems Using Deep Learning Model. In 2023 4th International Conference on Intelligent Technologies (CONIT) (pp. 1-5). IEEE.